



Client is in the semiconductor industry by developing strategic competencies, innovative technologies and intellectual property; enabling enterprises to be technologically competitive; and cultivating a technology talent pool to inject new knowledge to the industry.

## **SEMICONDUCTOR PROCESS TECHNOLOGIES LABORATORY**

### **Senior/Technical Manager - Materials Devices Reliability & Analysis**

#### **Responsibilities:**

- The candidate shall lead a team of engineering staff to develop strong research programs in the area of nanotechnology characterization by all available semiconductors, organic and inorganic materials, analytical tools, including the advanced Electron Microscopy, TEM/EELS for NanoElectronics, NanoPhotonics, MEMS, and Bio/Medical Devices.
- The candidate shall be able to plan strategic programs for Failure analysis and Reliability both in "More-Moore" and " Moore-than-Moore" domains, with strong background such as in advanced CMOS technology (e.g., 90nm, 65nm), MEMS/NEMS, Optoelectronics, and also advanced packaging technology. The candidate should not only support the existing semiconductor industry, but also help to expedite the new industry development.
- The candidate shall leverage on the established programs to develop strong industry alliance and partnership to continuously apply the advanced analytical tools to the above mentioned technology areas, to go beyond the "More-Moore" domain.
- The candidate shall work with other programs and Labs to identify the critically needed areas that require advanced analytical techniques, particularly in areas of Advanced Packaging, Bio/Medical Devices, nano-Structure and Devices, to help shed new insights to the device performance and reliability.



**Requirements:**

- PhD in solid state physics or material science, or Electrical and Electronic Engineering, Chemistry or Chemical engineering
- At least 8 years of industrial experience in relevant industries, such as IC manufacturing lines, Integration and Yield-Improvement, with at least 3 years in a leadership/management role
- Knowledge of applying advanced analytical tools for Yield-Improvement is preferred
- Experience in STEM EELS and in EELS spectrum analysis and its applications in the semiconductor area
- Knowledge in XPS and semiconductor devices & processes, including Bioelectronics is preferred

**Interested candidates are invited to submit your latest updated resume stating your availability of employment, current, achievements and expected salary to Adrian Collin Png at: [adrian@collincrawford.com](mailto:adrian@collincrawford.com)**